


U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE Washington, D.C. 20231  <b>INFORMATION DISCLOSURE          STATEMENT BY APPLICANT</b>	ATTY. DOCKET NO.	Serial No.
	MTI-31529	09/935,255
	Applicant	Confirmation No.
	Ronald A. Weimer	1208
	Filing Date	Group Art Unit
	08/22/2001	2813

## U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Publication/ Issue Date	Patentee	U.S. Class	Sub-Class

## OTHER DOCUMENTS (Including Author, Title, Date, Relevant Pages, Place of Publication)

Examiner Initial	Non-Patent Document
C1	Wolf, Silicon Processing for the VLSI Era, Vol. 2-Process Integration, Lattice Press: Sunset Beach CA, 1990, pp. 332-335

Examiner Initials 	Date Considered <u>5/9/06</u>
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

March 15, 2006

Patent

Attorney Docket No. MTI-31529

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Examiner Initial		Document No.	Publication/ Issue Date	Patentee	U.S. Class	Sub-Class
~	A1	5,563,093	10-08-96	Koda et al.	438	231
I	A2	6,005,807	12-21-99	Chen	365	185.26
I	A3	6,087,229	07-11-00	Aronowitz et al.	438	591
~	A4	6,323,114 B1	11-27-01	Hattangady et al.	438	591

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MKE/932574.1

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Patent

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*already  
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